

Mar. 16, 2007 Preliminarv

22Mbps SFP Transceiver with Spring Latch

(With monitoring function, for $15 \text{km} \sim 80 \text{km}$ transmission)



Features

- Up to 622Mbps data-rate
- 1310nm FP laser and PIN photodetector for 15km and 40km transmission
- 1310nm DFB laser and PIN photodetector for 40km transmission
- and PIN 1550nm uncooled DFB laser photodetector for 80km transmission
- Digital diagnostic monitor interface compatible with SFF-8472
- SFP MSA package with duplex LC connector
- With spring latch for easily removing
- Very low EMI and excellent ESD protection
- +3.3V single power supply
- Operating case temperature: 0 to +70°C

Applications

- SDH STM-4, S-4.1, L-4.1, L-4.2
- SONET OC-12 IR1, LR1, LR2
- Other optical links

Standard

- Members of Flexon[™] Family
- Compatible with Telcordia GR-253-CORE
- Compatible with FCC 47 CFR Part 15, Class B
- Compatible with FDA 21 CFR 1040.10 and 1040.11, Class I
- Compliant with RoHS

Description

Fiberxon 622Mbps Spring-latch SFP transceiver is high performance, cost effective module that supports data-rate of 622Mbps and transmission distance from 15km to 80km.

The transceiver consists of two sections: The transmitter section incorporates a FP or uncooled DFB laser, and the receiver section consists of a PIN photodiode integrated with a trans-impedance preamplifier (TIA). All modules satisfy class I laser safety requirements.

The optical output can be disabled by a TTL logic high-level input of Tx Disable. Tx Fault is provided to indicate degradation of the laser. Loss of signal (LOS) output is provided to indicate the loss of an input optical signal of receiver.

An enhanced Digital Diagnostic Monitoring Interface Compatible with SFF-8472 has been incorporated into the transceivers. It allows real time access to the transceiver operating parameters such as transceiver temperature, laser bias current. transmitted optical power, received optical power and transceiver supply voltage by reading a built-in memory with I²C interface. For further information, please refer to SFF-8472 Rev 9.5.

- Compatible with SFP MSA
- Compatible with SFF-8472 Rev 9.5
- Compatible with ITU-T G.957 and G.958

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Regulatory Compliance

The transceivers have been tested according to American and European product safety and electromagnetic compatibility regulations (See Table 1). For further information regarding regulatory certification, please refer to Fiberxon regulatory specification and safety guidelines, or contact with Fiberxon, Inc. America sales office listed at the end of the documentation.

Feature	Standard	Performance
Electrostatic Discharge	MIL-STD-883E	Class 2(>2000 V)
(ESD) to the Electrical Pins	Method 3015.7	Class $2(-2000 \text{ v})$
Electrostatic Discharge (ESD)	IEC 61000-4-2	Compatible with standards
to the Duplex LC Receptacle	GR-1089-CORE	Compatible with standards
Flootromognotio	FCC Part 15 Class B	
Electromagnetic	EN55022 Class B (CISPR 22B)	Compatible with standards
Interference (EMI)	VCCI Class B	
Immunity	IEC 61000-4-3	Compatible with standards
Lagar Eva Safaty	FDA 21CFR 1040.10 and 1040.11	Compatible with Class 1 laser
Laser Eye Safety	EN60950, EN (IEC) 60825-1,2	product.
Component Recognition	UL and CSA	Compatible with standards
RoHS	2002/95/EC 4.1&4.2	Compliant with standards note
RUNO	2005/747/EC	

Table 1- Regulatory Compliance

Note:

In light of item 5 in Annex of 2002/95/EC, "Pb in the glass of cathode ray tubes, electronic components and fluorescent tubes." and item 13 in Annex of 2005/747/EC, "Lead and cadmium in optical and filter glass.", the two exemptions are being concerned for Fiberxon's transceivers, because Fiberxon's transceivers use glass, which may contain Pb, for components such as lenses, windows, isolators, and other electronic components.

Absolute Maximum Ratings

Stress in excess of the maximum absolute ratings can cause permanent damage to the module.

Table 2 - Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	Τs	-40	+85	°C
Supply Voltage	V _{CC}	-0.5	3.6	V
Operating Relative Humidity	-	5	95	%

Recommended Operating Conditions

Table 3- Recommended Operating Conditions

622Mbps Spring-latch SFP Transceiver

 $15{\sim}80$ km transmission with Monitoring function

Preliminary Datasheet

Mar 16, 2007

Fiberxon

Parameter	Symbol	Min.	Typical	Max.	Unit
Operating Case Temperature	Tc	0		+70	°C
Power Supply Voltage	V _{CC}	3.13		3.47	V
Power Supply Current	I _{CC}			300	mA
Data Rate			622		Mbps

Optical and Electrical Characteristics

All parameters are specified at overall operating case temperature and power supply range, unless otherwise stated.

FTM-3106C-SL15G (1310nm FP and PIN, 15km, Monitoring function)

Table 4 - Optical and Electrical Characteristics (Tc=25°C, Vcc=3.3V)

Table 4 - Optical				-0.0 4)		\	
Parameter		Symbol	Min.	Typical	Max.	Unit	Notes
		T	ransmitter				
Centre Waveleng	gth	λ _C	1274		1356	nm	
Average Output	Power	P _{0ut}	-15		-8	dBm	1
Spectral Width (F	RMS)	σ		0r	2.5	nm	
Extinction Ratio		EX	8.2			dB	
Jitter Generation	(RMS)				0.01	UI	
Jitter Generation	(pk-pk)		2		0.1	UI	
Output Optical E	ye	Compatib	le with Telco	ordia GR-253 G.957	3-CORE and	ITU-T	2
Data Input Swing	g Differential	V _{IN}	300		1860	mV	3
Input Differential	Impedance	Z _{IN}	90	100	110	Ω	
TYDiashla	Disable		2.0		Vcc	V	
TX Disable	Enable		0		0.8	V	
TXE	Fault		2.0		Vcc+0.3	V	
TX Fault	Normal		0		0.8	V	
		-	Receiver				
Centre Waveleng	gth	λ _C	1260		1580	nm	
Receiver Sensitiv	vity				-28	dBm	4
Receiver Overloa	ad		-8			dBm	4
Optical Path Pen	alty				1	dB	5
LOS De-Assert		LOSD			-31	dBm	
LOS Assert		LOS _A	-42			dBm	
LOS Hysteresis			1		4	dB	
Data Output Swir	ng Differential	V _{OUT}	370		1800	mV	6
LOS	High		2.0		Vcc+0.3	V	
103	Low		0		0.8	V	

Notes:

1. The optical power is launched into SMF.

Preliminary Datasheet

-iberxon Mar 16, 2007

 $15{\sim}80$ km transmission with Monitoring function

- Measured with a PRBS 2²³-1 test pattern @622Mbps. 2.
- Internally AC coupled and terminated. 3.
- Measured with a PRBS 2²³-1 test pattern @622Mbps, BER $\leq 1 \times 10^{-10}$. 4.
- Measured with a PRBS 2^{23} -1 test pattern @622Mbps, over 15km G.652 SMF, BER $\leq 1 \times 10^{-10}$. 5.
- 6. Internally AC coupled.

FTM-3106C-SL40G (1310nm FP and PIN, 40km, Monitoring function)

Table 5 - Optical and Electrical Characteristics (Tc=25°C, Vcc=3.3V)								
Para	neter	Symbol	Min.	Typical	Max.	Unit	Notes	
		٦	ransmitter					
Centre Waveleng	Ith	λ _C	1274		1356	nm	\geq	
Average Output F	Power	P _{0ut}	-3	5	+2	dBm	1	
Spectral Width (F	RMS)	σ		()	2.5	nm		
Extinction Ratio		EX	10			dB		
Jitter Generation	(RMS)				0.01	UI		
Jitter Generation	(pk-pk)		111 110		0.1	UI		
Output Optical Ey	/e	Compatil	ole with Telco	rdia GR-253 G.957	3-CORE and	ITU-T	2	
Data Input Swing	Differential	Vin	300		1860	mV	3	
Input Differential	Impedance	Zin	90	100	110	Ω		
TX Disable	Disable		2.0		Vcc	V		
TA DISable	Enable		0		0.8	V		
TX Fault	Fault	/	2.0		Vcc+0.3	V		
IA Fault	Normal		0		0.8	V		
			Receiver					
Centre Waveleng	ith	λ _C	1260		1580	nm		
Receiver Sensitiv	vity				-28	dBm	4	
Receiver Overloa	ad		-8			dBm	4	
Optical Path Pen	alty				1	dB	5	
LOS De-Assert		LOSD			-31	dBm		
LOS Assert		LOS _A	-42			dBm		
LOS Hysteresis			1		4	dB		
Data Output Swin	g Differential	V _{OUT}	370		1800	mV	6	
1.05	High		2.0		Vcc+0.3	V		
LOS	Low		0		0.8	V		

Table 5 - Ontical and Electrical Characteristics ($T_{a}=25^{0}C$)/a=3 3)/)

Notes:

- 1. The optical power is launched into SMF.
- 2. Measured with a PRBS 2²³-1 test pattern @622Mbps.
- 3. Internally AC coupled and terminated.
- 4. Measured with a PRBS 2²³-1 test pattern @622Mbps, BER $\leq 1 \times 10^{-10}$.
- 5. Measured with a PRBS 2^{23} -1 test pattern @622Mbps, over 40km G.652 SMF, BER $\leq 1 \times 10^{-10}$.

Preliminary Datasheet



6. Internally AC coupled.

FTM-3106C-SL40DG (1310nm DFB and PIN, 40km, Monitoring function)

Table 6 - Optical and Electrical Characteristics

Para	meter	Symbol	Min.	Typical	Max.	Unit	Notes
		T	ransmitter				
Centre Waveleng	gth	λ _C	1280		1335	nm	~
Average Output	Power	P _{0ut}	-3		+2	dBm	1
Spectral Width (-	-20dB)	Δλ			1	nm	$\sim//$
Side Mode Supp	ression Ratio	SMSR	30			dB	
Extinction Ratio		EX	10		1/1	dB	~
Jitter Generation	(RMS)				0.01	UI	
Jitter Generation	ı (pk-pk)			[1	0.1	U	
Output Optical E	ye	Compatib	le with Telco	rdia GR-253 G.957	-CORE and	ITU-T	2
Data Input Swing	g Differential	V _{IN}	300		1860	mV	3
Input Differential	Impedance	Z _{IN}	90	100	110	Ω	
TX Disable	Disable	14	2.0		Vcc	V	
	Enable		0		0.8	V	
TX Fault	Fault		2.0		Vcc+0.3	V	
TA Fault	Normal		0		0.8	V	
			Receiver				
Centre Wavelen	gth	λ _c	1260		1580	nm	
Receiver Sensiti	vity				-28	dBm	4
Receiver Overloa	ad		-8			dBm	4
Optical Path Penalty					1	dB	5
LOS De-Assert		LOSD			-31	dBm	
LOS Assert		LOS _A	-42			dBm	
LOS Hysteresis			1		4	dB	
Data Output Swin	ng Differential	V _{OUT}	370		1800	mV	6
LOS	High		2.0		Vcc+0.3	V	
103	Low		0		0.8	V	

Notes:

- 1. The optical power is launched into SMF.
- 2. Measured with a PRBS 2²³-1 test pattern @622Mbps.
- 3. Internally AC coupled and terminated.
- 4. Measured with a PRBS 2²³-1 test pattern @622Mbps, BER $\leq 1 \times 10^{-10}$.
- 5. Measured with a PRBS 2^{23} -1 test pattern @622Mbps, over 40km G.652 SMF, BER $\leq 1 \times 10^{-10}$.
- 6. Internally AC coupled.



Preliminary Datasheet

FTM-5106C-SL80G (1550nm DFB and PIN, 80km, Monitoring function)

Para	ameter	Symbol	Min.	Typical	Max.	Unit	Notes
		Т	ransmitter				
Centre Waveler	ngth	$\lambda_{\rm C}$	1480		1580	nm	
Average Output	Power	P _{0ut}	-3		+2	dBm	1
Spectral Width	(-20dB)	Δλ			1	nm	<
Side Mode Sup	pression Ratio	SMSR	30			dB	
Extinction Ratio		EX	10			dB	
Jitter Generatio	n (RMS)				0.01 🦯	U	
Jitter Generatio	n (pk-pk)				0.1	וט	<
Output Optical I	Еуе	Compatib	le with Telco	rdia GR-253 G.957	-CORE and	ТО-Т	2
Data Input Swir	g Differential	V _{IN}	300		1860	mV	3
Input Differentia	I Impedance	Z _{IN}	90	100	110	Ω	
	Disable		2.0		Vcc	V	
TX Disable	Enable	/	0		0.8	V	
TX Fault	Fault	14	2.0		Vcc+0.3	V	
I A Fault	Normal		0		0.8	V	
			Receiver				
Centre Waveler	ngth	λ	1260		1580	nm	
Receiver Sensit	ivity				-28	dBm	4
Receiver Overlo	bad		-8			dBm	
Optical Path Pe	nalty	/			1	dB	5
LOS De-Assert		LOS _D			-31	dBm	
LOS Assert	1 AL D	LOS _A	-42			dBm	
LOS Hysteresis	\square		1		4	dB	
Data Output Sw	ing Differential	V _{OUT}	370		1800	mV	6
LOS	High		2.0		Vcc+0.3	V	
105	Low		0		0.8	V	

Table 7 - Optical and Electrical Characteristics

Notes:

- 1. The optical power is launched into SMF.
- 2. Measured with a PRBS 2²³-1 test pattern @622Mbps.
- 3. Internally AC coupled and terminated.
- 4. Measured with a PRBS 2²³-1 test pattern @622Mbps, BER $\leq 1 \times 10^{-10}$.
- 5. Measured with a PRBS 2²³-1 test pattern @622Mbps, over 80km G.652 SMF, BER $\leq 1 \times 10^{-10}$.
- 6. Internally AC coupled.

EEPROM Information

The SFP MSA defines a 256-byte memory map in EEPROM describing the transceiver's capabilities, standard interfaces, manufacturer, and other information, which is accessible over a two-wire serial interface at the 8-bit address 1010000X (A0h). For memory contents please refer to Table 8.

Addr.	Ir. Field Size (Bytes) Name of Field		Hex	Description
0	1	Identifier	03	SFP
1	1	Ext. Identifier	04	MOD4
2	1	Connector	07	LC
3—10	8	Transceiver	00 xx xx 00 00 00 00 00	OC 12, Single mode inter. or long reach
11	1	Encoding	03	NRZ
12	1	BR, nominal	06	622Mbps
13	1	Reserved	00	
14	1	Length (9um)-km	xx	15km/40km/80km(0F/28/50)
15	1	Length (9um)	xx	15km/40km/80km(96/FF/FF)
16	1	Length (50um)	00	
17	1	Length (62.5um)	00	
18	1	Length (copper)	00 00	
19	1	Reserved	00	
20—35	16	Vendor name	46 49 42 45 52 58 4F 4E 20 49 4E 43 2E 20 20 20	"FIBERXON INC. "(ASCⅡ)
36	1	Reserved	00	
37—39	3	Vendor OUI	00 00 00	
40—55	16	Vendor PN	46 54 4D 2D xx 31 30 36 43 2D 53 4C xx xx xx 47	"FTM-x106C-SLxxxG " (ASC Ⅱ)
56—59	4	Vendor rev	xx xx 20 20	ASC II ("31 30 20 20" means 1.0 revision)
60-61	2	Wavelength	05 1E/06 0E	1310nm/1550nm
62		Reserved	00	
63	リ 1	CC BASE	xx	Check sum of bytes 0 - 62
64—65	2	Options	00 1A	LOS, TX_FAULT and TX_DISABLE
66	1	BR, max	00	
67	1	BR, min	00	
68—83	16	Vendor SN	xx xx xx xx xx xx xx xx xx xx xx xx xx x	ASC II ,
84—91	8	Vendor date code	xx xx xx xx xx xx 20 20	Year (2 bytes), Month (2 bytes), Day (2 bytes)
92	1	Diagnostic type	58	Diagnostics(Ext.Cal)
93	1	Enhanced option	В0	Diagnostics (Optional Alarm/warning flags, Soft TX_FAULT and Soft TX_LOS monitoring)
94	1	SFF-8472	02	Diagnostics(SFF-8472 Rev 9.4)
95	1	CC EXT	xx	Check sum of bytes 64 - 94
96—255	160	Vendor specific		-

Table 8 - EEPROM Serial ID Memory Contents (A0h)

Note: The "xx" byte should be filled in according to practical case. For more information, please refer to the

Preliminary Datasheet

Mar 16, 2007

-iberxon

related document of SFF-8472 Rev 9.5.

Monitoring Specification

The digital diagnostic monitoring interface also defines another 256-byte memory map in EEPROM, which makes use of the 8 bit address 1010001X (A2h). Please see Figure 1. For detail EEPROM information, please refer to the related document of SFF-8472 Rev 9.5. The monitoring specification of this product is described in Table 9

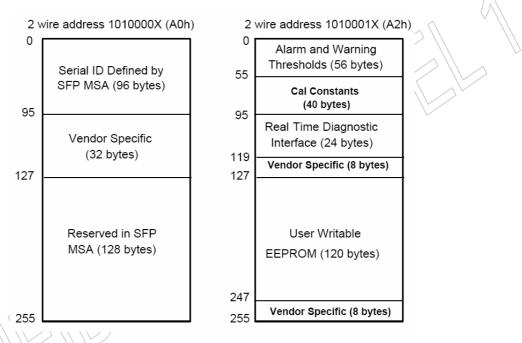


Figure 1, EEPROM Memory Map Specific Data Field Descriptions

Table 9- Monitoring Specification

Parameter		Range	Accuracy	Calibration	
Te	emperature	-10 to 80°C	±3°C	External	
	Voltage	3.0 to 3.6V	±3%	External	
В	ias Current	0 to 100mA	±10%	External	
	FTM-3106C-SL15G	-16 to –7 dBm			
TX Power	FTM-3106C-SL40G	-4 to +3 dBm	±3dB	External	
TAFOWEI	FTM-3106C-SL40DG	-4 to +3 dBm	TOUD		
	FTM-5106C-SL80G	-4 to +3 dBm			
	FTM-3106C-SL15G	-30 to –7 dBm			
RX Power	FTM-3106C-SL40G	-30 to –7 dBm	±3dB	External	
RX Power	FTM-3106C-SL40DG	-30 to –7 dBm	IJUD	External	
	FTM-5106C-SL80G	-30 to –7 dBm			

Fiber×00 Mar 16, 2007

 $15{\sim}80$ km transmission with Monitoring function

Recommended Host Board Power Supply Circuit

Figure 2 shows the recommended host board power supply circuit.

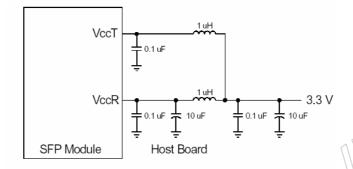


Figure 2, Recommended Host Board Power Supply Circuit

Recommended Interface Circuit

Figure 3 shows the recommended interface circuit.

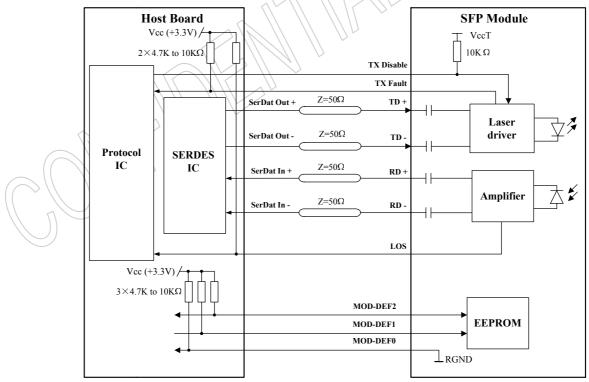


Figure 3, Recommended Interface Circuit

Pin Definitions

Figure 4 below shows the pin numbering of SFP electrical interface. The pin functions are described in Table 10 with some accompanying notes.

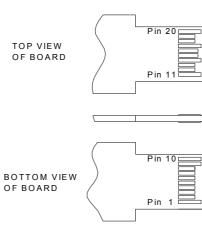
622Mbps Spring-latch SFP Transceiver



15~80 km transmission with Monitoring function

Preliminary Datasheet

Mar 16, 2007



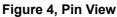


Table 10 - Pin Function Definitions

Pin No.	Name	Function	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	>
2	TX Fault	Transmitter Fault Indication	3	Note 1
3	TX Disable	Transmitter Disable	3	Note 2
4	MOD-DEF2	Module Definition 2	3	Note 3
5	MOD-DEF1	Module Definition 1	3	Note 3
6	MOD-DEF0	Module Definition 0	3	Note 3
7	Rate Select	Not Connected	3	
8	LOS	Loss of Signal	3	Note 4
9	VeeR	Receiver Ground	1	
10	VeeR	Receiver Ground	1	
11	VeeR	Receiver Ground	1	
12	RD-	Inv. Received Data Out	3	Note 5
13	RD+	Received Data Out	3	Note 5
14	VeeR	Receiver Ground	1	
15	VccR	Receiver Power	2	
16	VccT	Transmitter Power	2	
17	VeeT	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 6
19	TD-	Inv. Transmit Data In	3	Note 6
20	VeeT	Transmitter Ground	1	

Notes:

- 1. TX Fault is an open collector output, which should be pulled up with a $4.7k\sim10k\Omega$ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2. TX Disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a $4.7k \sim 10k\Omega$ resistor. Its states are:
 - Low (0~0.8V): Transmitter on (>0.8V, <2.0V): Undefined High (2.0~3.465V):

Transmitter Disabled Fiberxon Proprietary and Confidential, Do Not Copy or Distribute Page 10 of 13

Preliminary Datasheet

Mar 16, 2007

Open:

Transmitter Disabled

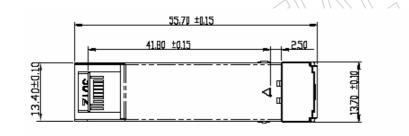
 MOD-DEF 0,1,2 are the module definition pins. They should be pulled up with a 4.7k~10kΩ resistor on the host board. The pull-up voltage shall be VccT or VccR.
 MOD-DEF 0 grounded by the module indicates that the module is present

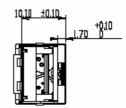
MOD-DEF 1 is the clock line of two-wire serial interface for serial ID MOD-DEF 2 is the data line of two-wire serial interface for serial ID

- LOS is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; logic 1 indicates loss of signal. In the low state, the output will be pulled to less than 0.8V.
- 5. These are the differential receiver outputs. They are internally AC-coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 6. These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module.

Mechanical Design Diagram

The mechanical design diagram is shown in Figure 5.





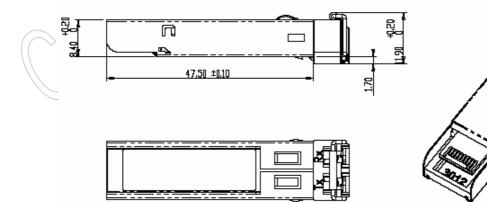


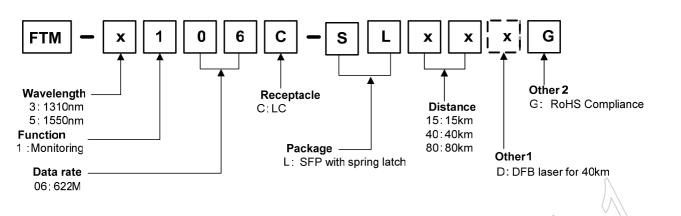
Figure 5, Mechanical Design Diagram of the SFP with Spring Latch

Ordering information

Preliminary Datasheet



Mar 16, 2007



Part No.	Product Description
FTM-3106C-SL15G	1310nm, 622Mbps, 15km, SFP with spring latch, Monitoring function, 0°C~+70°C,RoHS
FTM-5100C-3E13G	Compliance
FTN 04000 01 400	1310nm, 622Mbps, 40km, SFP with spring latch, Monitoring function, 0°C~+70°C,RoHS
FTM-3106C-SL40G	Compliance
FTM-3106C-SL40DG	1310nm, 622Mbps, 40km, SFP with spring latch, Monitoring function, DFB, 0°C~+70°C,RoHS
FTM-5100C-3L40DG	Compliance
FTM-5106C-SL80G	1550nm, 622Mbps, 80km, SFP with spring latch, Monitoring function, 0°C~+70°C,RoHS
FTM-5100C-3L80G	Compliance

Related Documents

For further information, please refer to the following documents:

- Fiberxon SFP Application Notes
- SFP Multi-Source Agreement (MSA)
- SFF-8472 Rev 9.5

Obtaining Document

You can visit our website:

http://www.fiberxon.com

Or contact Fiberxon, Inc. America Sales Office listed at the end of documentation to get the latest documents.

Revision History

Revision	Initiate	Review	Approve	Subject	Release Date
Rev. 1a	Solaris Zhu	Simon Jiang	Walker.Wei	Initial datasheet	Mar 16, 2007

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622Mbps Spring-latch SFP Transceiver

 $15{\sim}80$ km transmission with Monitoring function

Preliminary Datasheet



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